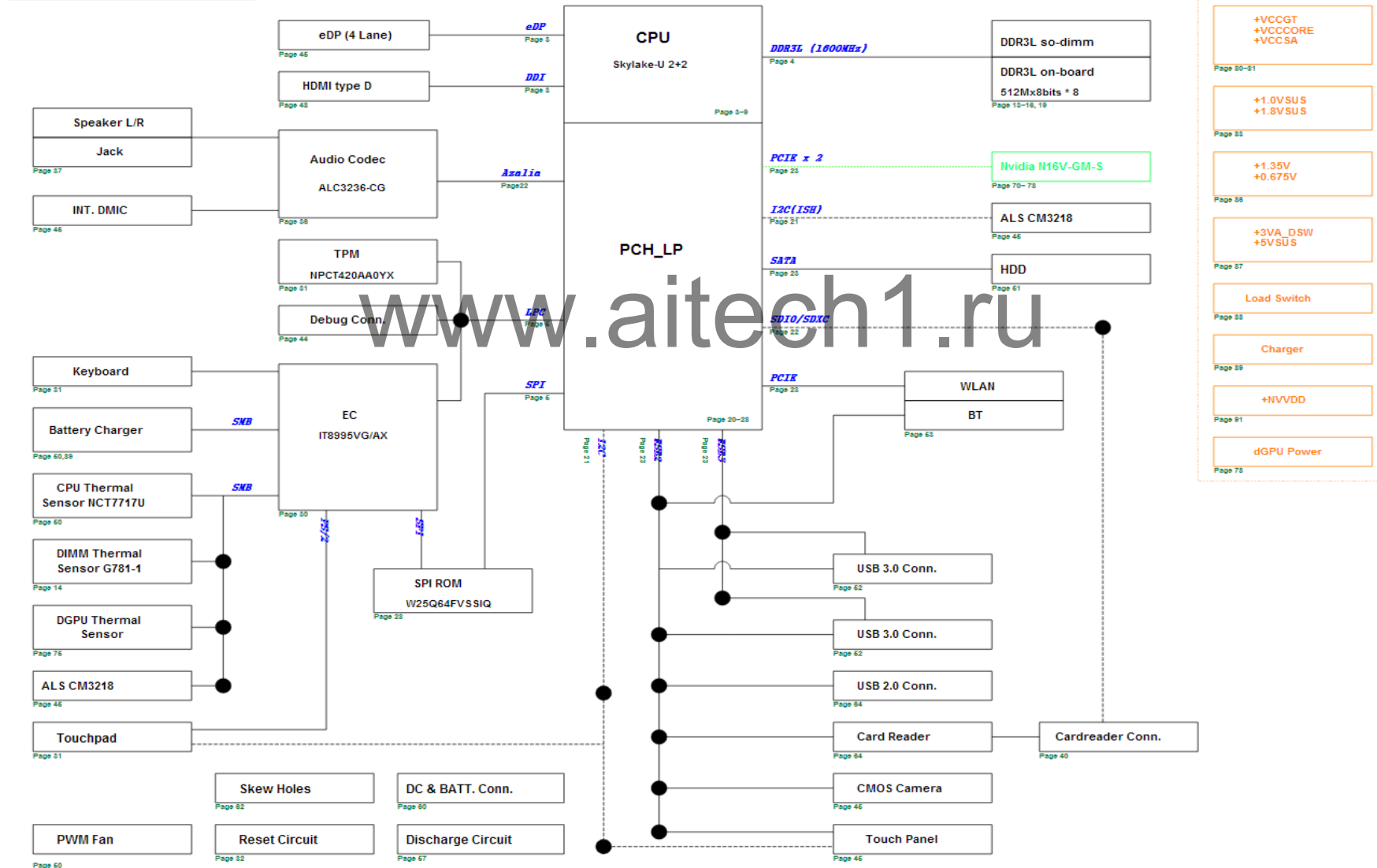


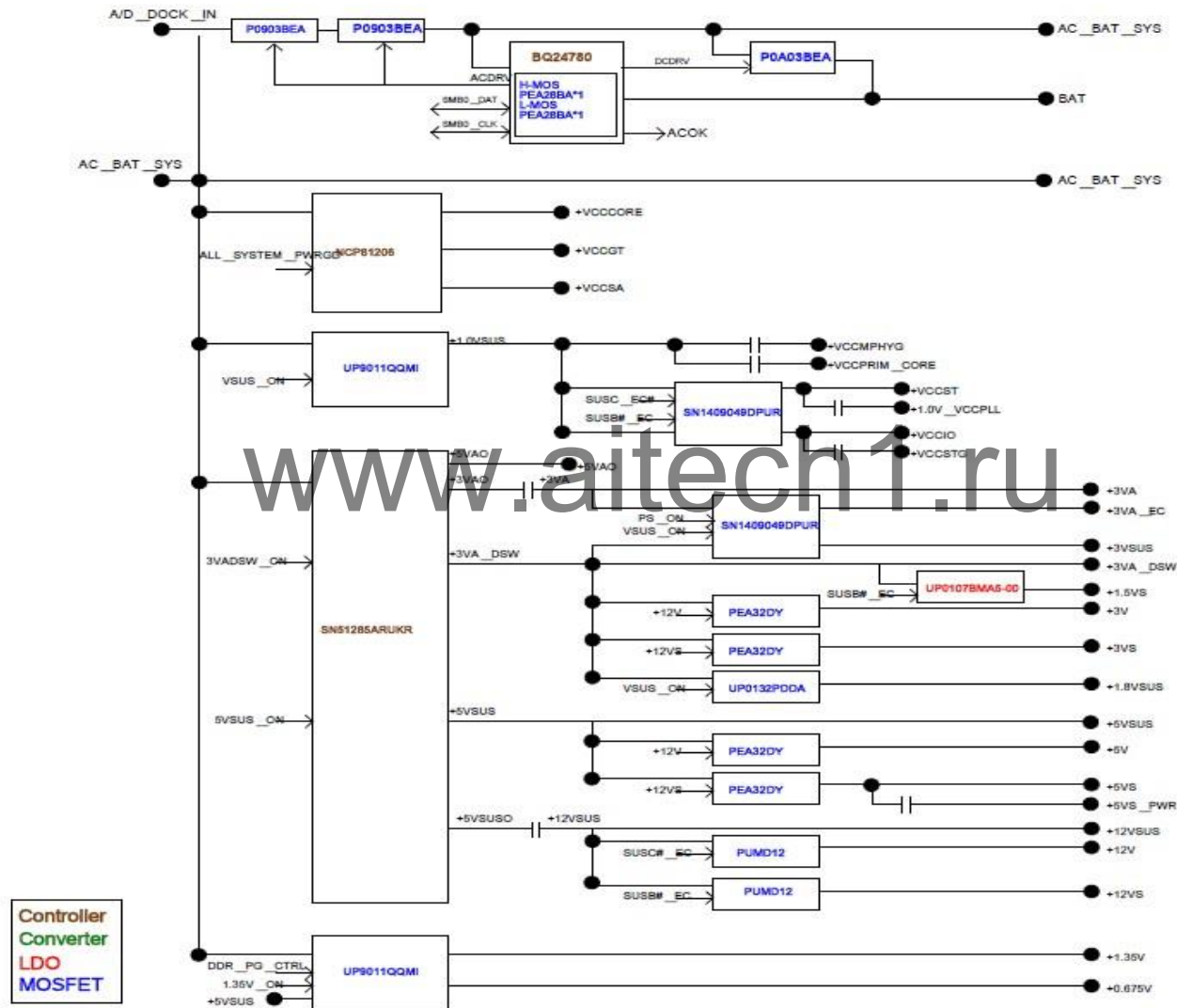
Q304UA SCHEMATIC Revision 2.0

BLOCK DIAGRAM

Non Connected Standby



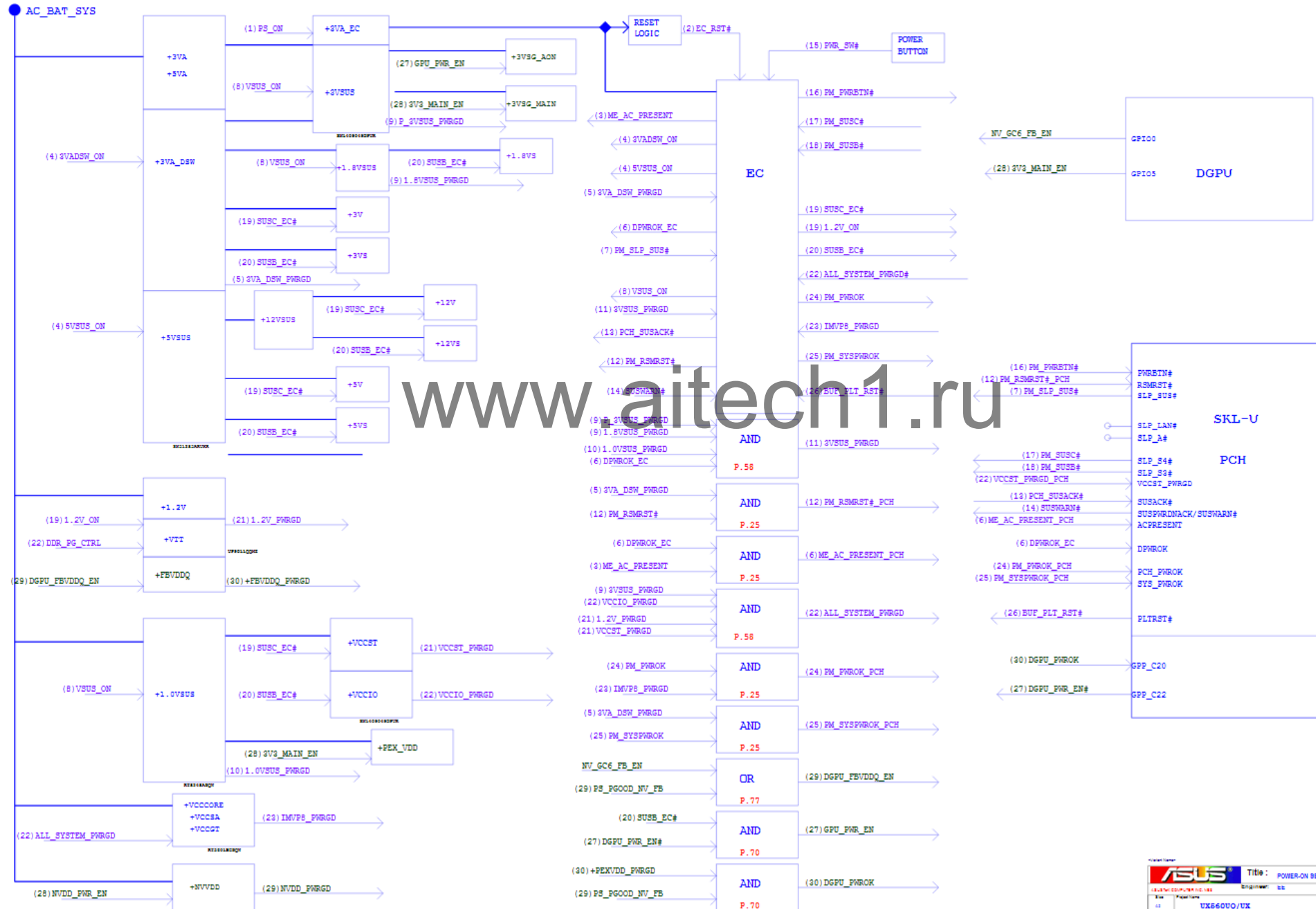
POWER FLOW



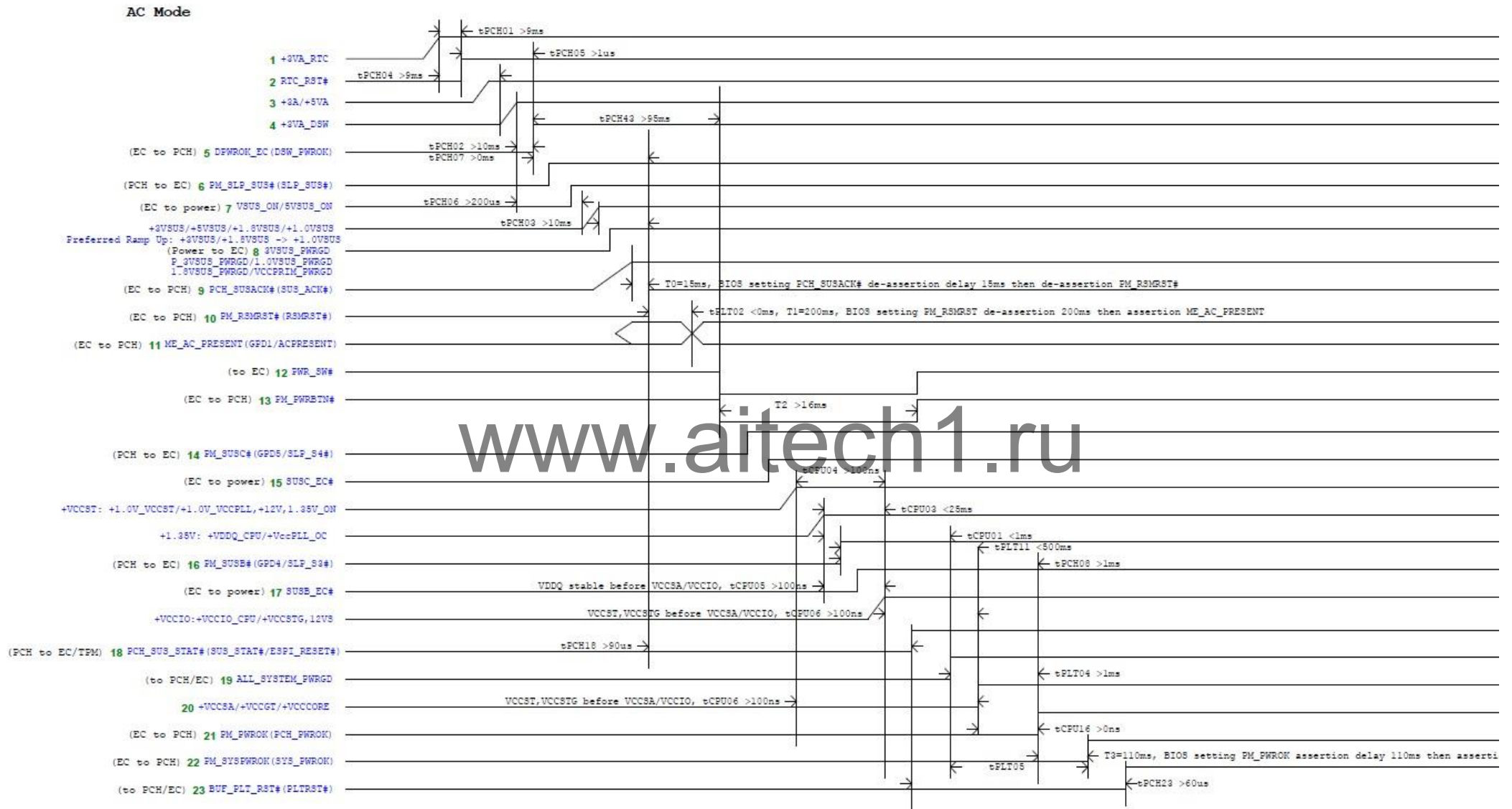
www.aitech1.ru

● AC_BAT_SYS

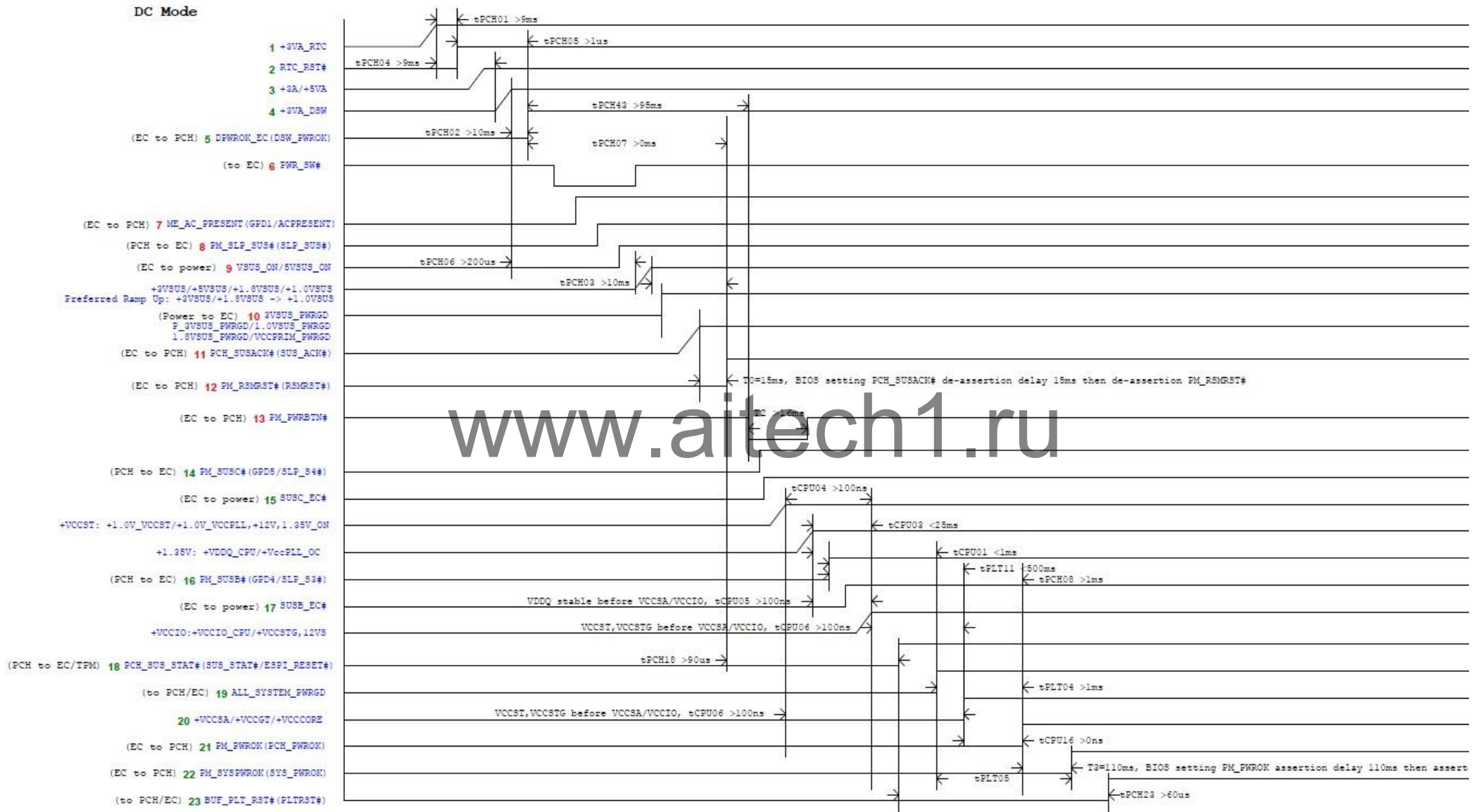
Diagram illustrating the pinmux configuration for the EC (Embedded Controller) block. The diagram shows various input and output pins connected to the EC block, which is labeled 'EC'. The inputs are on the left, and the outputs are on the right. The diagram is divided into sections by horizontal lines. The top section shows inputs like (2) ME_AC_PRESENT, (4) 3VADSW_ON, (4) 5VSUS_ON, (5) 2VA_DSW_FWRGD, (6) DPMRQK_EC, (7) PM_SLP_SUS#, (8) VSUS_ON, (11) 3VSUS_FWRGD, (13) PCH_SUSACK#, (12) PM_RSTMRST#, (14) 3VSUS_FWRGD, (9) 1.2VUS_FWRGD, (9) 1.2VSUS_FWRGD, (10) 1.0VSUS_FWRGD, (6) DPMRQK_EC, (5) 2VA_DSW_FWRGD, (12) PM_RSTMRST#, (6) DPMRQK_EC, (3) ME_AC_PRESENT, (9) 3VSUS_FWRGD, (22) VCCIO_FWRGD, (21) 1.2V_FWRGD, (21) VCCST_FWRGD, (24) PM_FWRQK, (23) IMVPS_FWRGD, (5) 2VA_DSW_FWRGD, (25) PM_SYSPWRQK, (29) PS_PGOOD_NV_FB, (20) SUBS_EC#, (27) DGPU_PWR_EN#, (30) +PEXVDD_FWRGD, and (29) PS_PGOOD_NV_FB. The outputs are on the right, including (15) PWR_SW#, (16) PM_FWRETN#, (17) PM_SUSC#, (18) PM_SUBS#, (19) SUSC_EC#, (19) 1.2V_ON, (20) SUBS_EC#, (22) ALL_SYSTEM_FWRGD#, (24) PM_FWRQK, (23) IMVPS_FWRGD, (25) PM_SYSPWRQK, (26) SUPPLT_RST#, (11) 3VSUS_FWRGD, (12) PM_RSTMRST#_PCH, (6) ME_AC_PRESENT_PCH, (22) ALL_SYSTEM_FWRGD, (24) PM_FWRQK_PCH, (25) PM_SYSPWRQK_PCH, (29) DGPU_FB/DDQ_EN, (27) GPU_PWR_EN, and (30) DGPU_FWRQK. The diagram also shows a 'RESET LOGIC' block connected to (2) EC_RST# and a 'POWER BUTTON' block connected to (15) PWR_SW#.



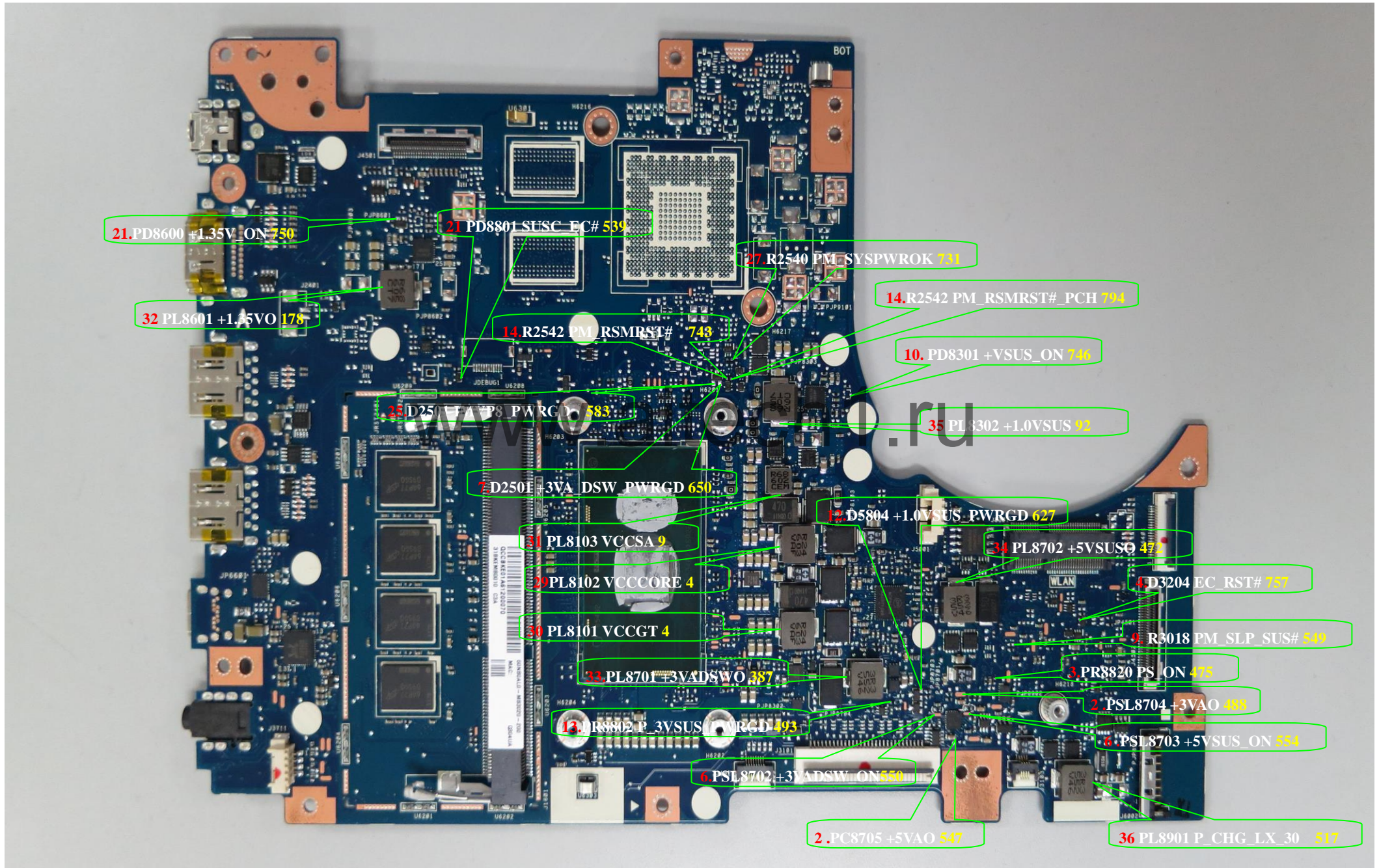
AC POWER ON SEQUENCE



www.aitech1.ru



Signal Measure Point-Bottom



Signal Measure Point-Top

